



PATENT

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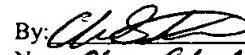
Applicant: ORTBERG ET AL. Examiner: NGUYEN, T.
Application No.: 09/977,626 Group Art Unit: 2746
Filing Date: OCTOBER 12, 2001 Docket No.: 2316.889USRE
Patent No.: 5,966,648
Issued: OCTOBER 12, 1999
Assignee: ADC TELECOMMUNICATION, INC
Title: RF CIRCUIT MODULE AND CHASSIS INCLUDING AMPLIFIER

CERTIFICATE UNDER 37 CFR 1.10

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I hereby certify that this correspondence is being deposited with the United States Postal Service 'Express Mail Post Office To Addressee' service under 37 CFR 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

By: 
Name: Chris Starckes

**DECLARATION OF GLEN BACKES IN
SUPPORT OF REISSUE APPLICATION**

I, Glen Backes, declare as follows:

1. I am a citizen of the United States of America residing at 7300 Gallagher Drive,
~~Savage~~ 55378
#327E, Edina, MN 55435, U.S.A. I believe that I am an original and first inventor, along with
Todd Charles Ortberg, a citizen of the United States of America, residing at 2221 Lukewood
Drive, Chanhassen, MN 55317, U.S.A.; and Zakhary Bluband, a citizen of the United States of
America, residing at 10840 46 PL NO Plymouth, MN 55442, U.S.A., of the invention described
and claimed in U.S. Patent No. 5,966,648, granted on October 12, 1999, and described and
claimed in the accompanying application for which I solicit a reissue patent.

2. I have reviewed and understand the contents of the specification and claims of this application, including reissue claims 10-44 added by amendment filed with this declaration.

3. I acknowledge the duty to disclose information which is material to the examination of this application in accordance with 37 C.F.R. § 1.56.

4. I believe the above-identified patent is partly inoperative by reason of claiming less than I had a right to claim in the patent. Such inoperativeness includes the failure to prosecute and obtain claims commensurate with the scope of new claims 10-44 added by this reissue application.

5. It was an error to fail to include in the above-identified patent, claims that are directed to a chassis frame and module combination comprising:

a) an amplifier module having:

1) a housing defining an enclosed interior; said housing having a front face and an opposite rear face separated by opposite sidewalls and opposite end walls,

2) two coax connectors secured to said rear face with an outer shield of said coax connectors electrically coupled to said housing;

3) circuitry contained within said housing; said circuitry including an amplifier circuit selected to amplify a radio frequency signal supplied to one of said coax connectors and to provide an amplified radio frequency signal to the other of said coax connectors;

4) a power supply port operatively coupled to said amplifier circuit;

b) a chassis frame including a pair of spaced apart walls, said walls spaced apart by a distance substantially equal to the distance between said end walls of said module; each of said walls including a groove, sized to slidably receive one of said projecting flanges;

c) a power downconverter separate from said amplifier module, said power converter mounted to said chassis frame, said power downconverter electrically coupled to said power supply port of said amplifier module for powering said amplifier circuit.

28. A chassis frame and module combination comprising:
- a) an amplifier module having:
 - 1) a housing defining an enclosed interior; said housing having a front face and an opposite rear face separated by opposite sidewalls and opposite end walls;
 - 2) two coax connectors secured to said rear face with an outer shield of said coax connectors electrically coupled to said housing;
 - 3) circuitry contained within said housing; said circuitry including an amplifier circuit selected to amplify a radio frequency signal supplied to one of said coax connectors and to provide an amplified radio frequency signal supplied to one of said coax connectors and to provide an amplified radio frequency signal to the other of said coax connectors;
 - 4) a power supply port operatively coupled to said amplifier circuit;
 - b) a chassis frame including a pair of spaced apart walls, said walls spaced apart by a distance substantially equal to a distance between said end walls of said module;
 - c) retainer for retaining said housing in said chassis frame;
 - d) a power downconverter separate from said amplifier module, said power converter mounted to said chassis frame, said power downconverter electrically coupled to said power supply port of said amplifier module for powering said amplifier circuit.

6. All of the errors being corrected in the reissue application up to the time of filing this declaration arose without any deceptive intention on the part of the Applicants.

7. I do hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of the Title 18 of the United

States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date: Jan. 09, 2002

Glen Backes
Glen Backes